

MITSUBISHI IGBT MODULES

CM100RX-24A

HIGH POWER SWITCHING USE

CM100RX-24A

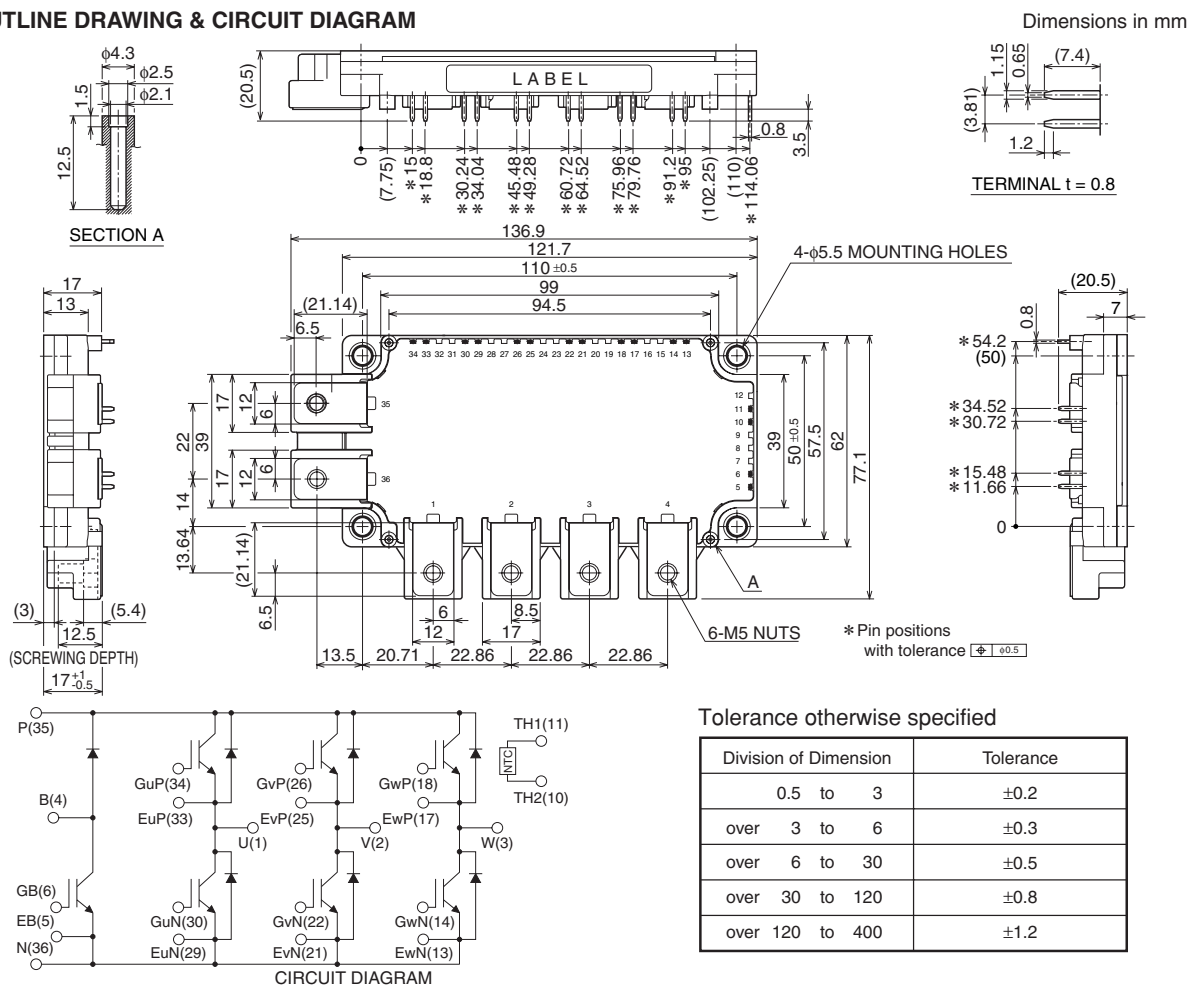


- Ic 100A
- VCES 1200V
- 7pack (3-phase Inverter + Brake)
- Flatbase Type / Insulated Package /
Copper (non-plating) base plate
- RoHS Directive compliant

APPLICATION

General purpose Inverters, Servo Amplifiers

OUTLINE DRAWING & CIRCUIT DIAGRAM



Jan. 2009

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HIGH POWER SWITCHING USE

ABSOLUTE MAXIMUM RATINGS (Tj = 25°C, unless otherwise specified)

INVERTER PART

Symbol	Parameter	Conditions	Rating	Unit
VCES	Collector-emitter voltage	G-E Short	1200	V
VGES	Gate-emitter voltage	C-E Short	±20	
IC	Collector current	DC, Tc = 90°C (Note. 1)	100	A
ICRM		Pulse (Note. 4)	200	
PC	Maximum collector dissipation	Tc = 25°C (Note. 1, 5)	625	W
IE (Note.3)	Emitter current	Tc = 25°C (Note. 1)	100	A
IERM(Note.3)	(Free wheeling diode forward current)	Pulse (Note. 4)	200	

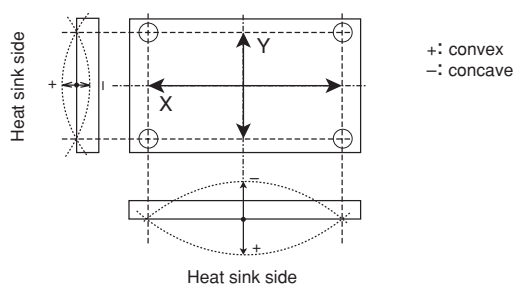
BRAKE PART

Symbol	Parameter	Conditions	Rating	Unit
VCES	Collector-emitter voltage	G-E Short	1200	V
VGES	Gate-emitter voltage	C-E Short	±20	
IC	Collector current	DC, Tc = 97°C (Note. 1)	50	A
ICRM		Pulse (Note. 4)	100	
PC	Maximum collector dissipation	Tc = 25°C (Note. 1, 5)	355	W
VRRM(Note.3)	Repetitive peak reverse voltage		1200	V
IF (Note.3)	Forward current	Tc = 25°C (Note. 1)	50	A
IFRM(Note.3)		Pulse (Note. 4)	100	

MODULE

Symbol	Parameter	Conditions	Rating	Unit
Tj	Junction temperature		-40 ~ +150	°C
Tstg	Storage temperature		-40 ~ +125	
Viso	Isolation voltage	Terminals to base plate, f = 60Hz, AC 1 minute	2500	Vrms
—	Base plate flatness	On the centerline X, Y (Note. 8)	±0 ~ +100	μm
—	Torque strength	Main terminals M5 screw	2.5 ~ 3.5	N·m
—	Torque strength	Mounting M5 screw	2.5 ~ 3.5	
—	Weight	(Typical)	330	g

Note. 8: The base plate flatness measurement points are in the following figure.



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ELECTRICAL CHARACTERISTICS (T_j = 25°C, unless otherwise specified)

INVERTER PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
ICES	Collector cutoff current	VCE = VCES, VGE = 0V	—	—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 10mA, VCE = 10V	6	7	8	V
IGES	Gate leakage current	±VGE = VGES, VCE = 0V	—	—	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	IC = 100A, VGE = 15V (Note. 6) T _j = 25°C	—	2.0	2.6	V
		T _j = 125°C	—	2.2	—	
		Chip	—	1.9	—	
Cies	Input capacitance	VCE = 10V VGE = 0V (Note. 6)	—	—	17.5	nF
Coes	Output capacitance		—	—	1.5	
Cres	Reverse transfer capacitance		—	—	0.34	
QG	Total gate charge	VCC = 600V, IC = 100A, VGE = 15V	—	500	—	nC
td(on)	Turn-on delay time	VCC = 600V, IC = 100A	—	—	100	ns
tr	Turn-on rise time	VGE = ±15V, RG = 3.0Ω	—	—	70	
td(off)	Turn-off delay time	Inductive load	—	—	300	
tf	Turn-off fall time		—	—	600	
trr (Note.3)	Reverse recovery time		—	—	150	
Qrr (Note.3)	Reverse recovery charge	(IE = 100A)	—	5	—	μC
VEC(Note.3)	Emitter-collector voltage	IE = 100A, VGE = 0V (Note. 6) T _j = 25°C	—	2.6	3.4	V
		T _j = 125°C	—	2.16	—	
		Chip	—	2.5	—	
Rth(j-c)Q	Thermal resistance (Junction to case) (Note. 1)	per IGBT	—	—	0.20	K/W
Rth(j-c)R		per free wheeling diode	—	—	0.29	
RGint	Internal gate resistance	Tc = 25°C, per switch	—	0	—	Ω
RG	External gate resistance		3	—	31	

BRAKE PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
ICES	Collector cutoff current	VCE = VCES, VGE = 0V	—	—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 5mA, VCE = 10V	6	7	8	V
IGES	Gate leakage current	±VGE = VGES, VCE = 0V	—	—	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	IC = 50A, VGE = 15V (Note. 6) T _j = 25°C	—	2.0	2.6	V
		T _j = 125°C	—	2.2	—	
		Chip	—	1.9	—	
Cies	Input capacitance	VCE = 10V VGE = 0V (Note. 6)	—	—	8.5	nF
Coes	Output capacitance		—	—	0.75	
Cres	Reverse transfer capacitance		—	—	0.17	
QG	Total gate charge	VCC = 600V, IC = 50A, VGE = 15V	—	250	—	nC
IRRM(Note.3)	Repetitive peak reverse current	VR = VRRM	—	—	1	mA
VFM(Note.3)	Forward voltage drop	IF = 50A (Note. 6) T _j = 25°C	—	2.6	3.4	V
		T _j = 125°C	—	2.16	—	
		Chip	—	2.5	—	
Rth(j-c)Q	Thermal resistance (Junction to case) (Note. 1)	per IGBT	—	—	0.35	K/W
Rth(j-c)R		per Clamp diode	—	—	0.48	
RGint	Internal gate resistance	Tc = 25°C	—	0	—	Ω
RG	External gate resistance		6.0	—	62	

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NTC THERMISTOR PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R	Zero power resistance	Tc = 25°C	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	Tc = 100°C, R100 = 493Ω	-7.3	—	+7.8	%
B(25/50)	B constant	Approximate by equation (Note. 7)	—	3375	—	K
P25	Power dissipation	Tc = 25°C	—	—	10	mW

MODULE

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Typ.	Max.	
Rth(c-f)	Contact thermal resistance (Case to fin) (Note. 1)	Thermal grease applied per 1 module (Note. 2)	—	0.015	—	K/W

Note. 1: Case temperature (Tc), heat sink temperature (Tf) measured point is just under the chips. (Refer to the figure of the chip location.)

2: Typical value is measured by using thermally conductive grease of λ = 0.9W/(m·K).

3: IE, IERM, VEC, trr and Qrr represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

IF, IFRM, VF, VRRM and IRRM represent ratings and characteristics of the Clamp diode of Brake part.

4: Pulse width and repetition rate should be such that the device junction temperature (Tj) dose not exceed Tjmax rating.

5: Junction temperature (Tj) should not increase beyond 150°C.

6: Pulse width and repetition rate should be such as to cause negligible temperature rise.

(Refer to the figure of the test circuit for VCE(sat) and VEC)

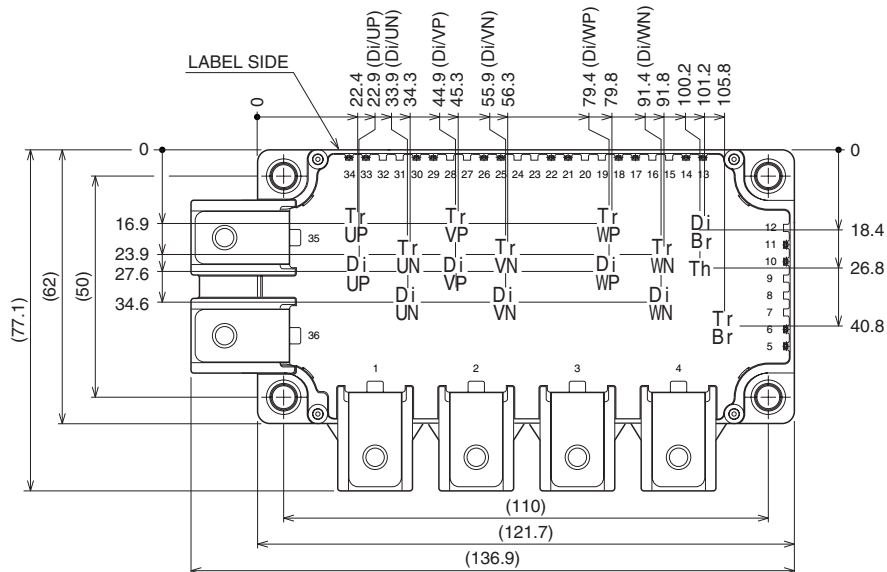
$$7: B(25/50) = \ln\left(\frac{R_{25}}{R_{50}}\right) / \left(\frac{1}{T_{25}} - \frac{1}{T_{50}}\right)$$

R25: resistance at absolute temperature T25 [K]; T25 = 25 [°C]+273.15 = 298.15 [K]

R50: resistance at absolute temperature T50 [K]; T50 = 50 [°C]+273.15 = 323.15 [K]

Chip Location (Top view)

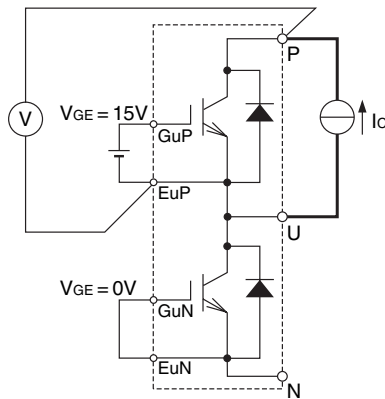
Dimensions in mm (tolerance: ±1mm)



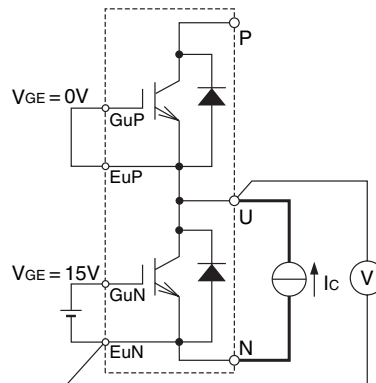
Each mark points the center position of each chip. Tr**: IGBT, Di**: FWDi (DiBr: Clamp diode), Th: NTC thermistor

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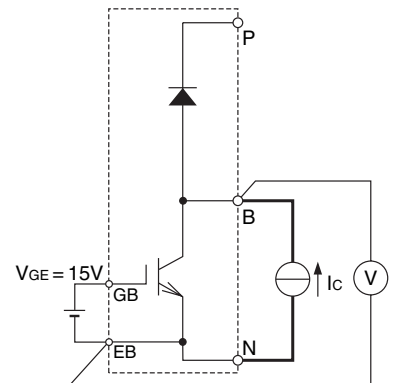
HIGH POWER SWITCHING USE



P side Inverter part Tr
(example of U arm)
 $V_{GE} = 0V (GvP-EvP, GwP-EwP, GvN-EvN, GwN-EwN, GB-EB)$

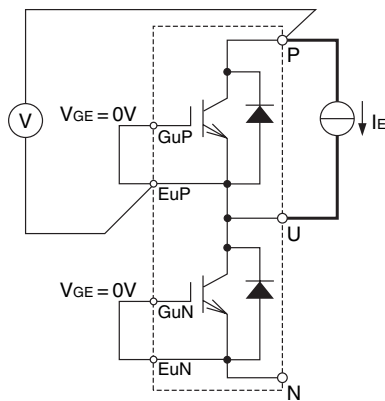


N side Inverter part Tr
(example of U arm)
 $V_{GE} = 0V (GvP-EvP, GwP-EwP, GvN-EvN, GwN-EwN, GB-EB)$

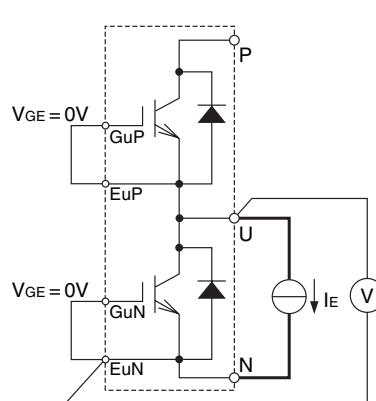


Br Tr
 $V_{GE} = 0V (GuP-EuP, GvP-EvP, GwP-EwP, GuN-EuN, GvN-EvN, GwN-EwN)$

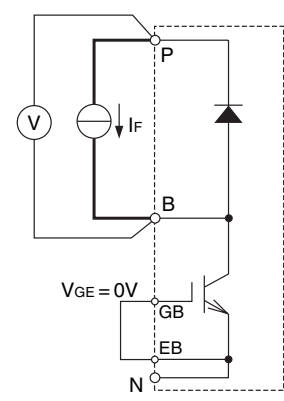
VCE(sat) test circuit



P side Inverter part Di
(example of U arm)
 $V_{GE} = 0V (GvP-EvP, GwP-EwP, GvN-EvN, GwN-EwN, GB-EB)$

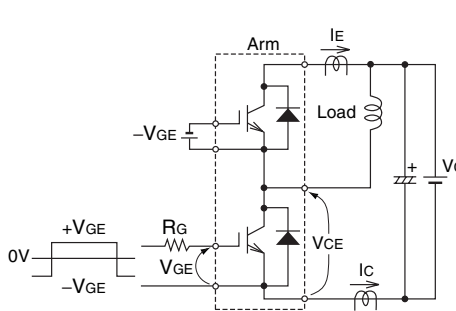


N side Inverter part Di
(example of U arm)
 $V_{GE} = 0V (GvP-EvP, GwP-EwP, GvN-EvN, GwN-EwN, GB-EB)$

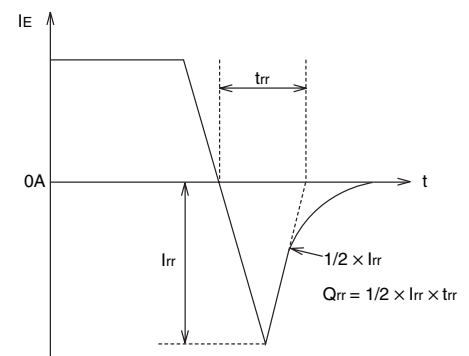
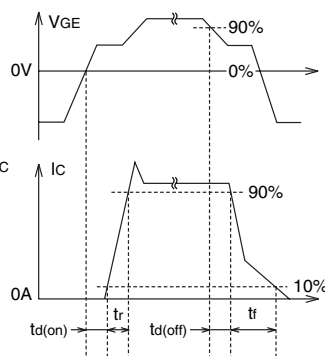


Br Di
 $V_{GE} = 0V (GuP-EuP, GvP-EvP, GwP-EwP, GuN-EuN, GvN-EvN, GwN-EwN)$

VEC/VFM test circuit



Switching time test circuit and waveforms

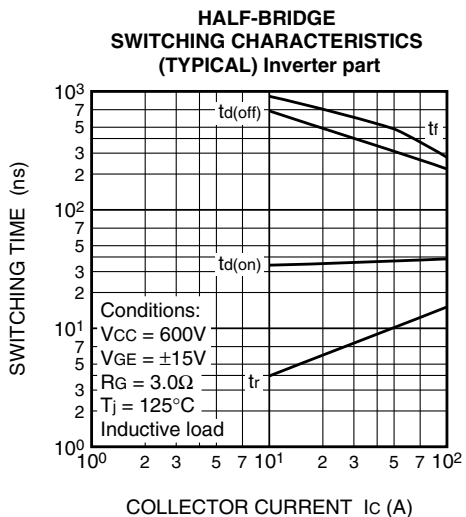
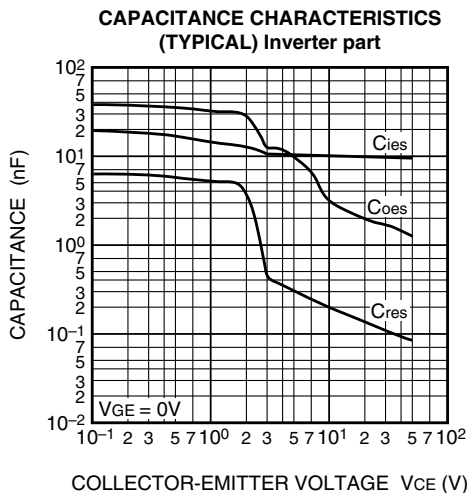
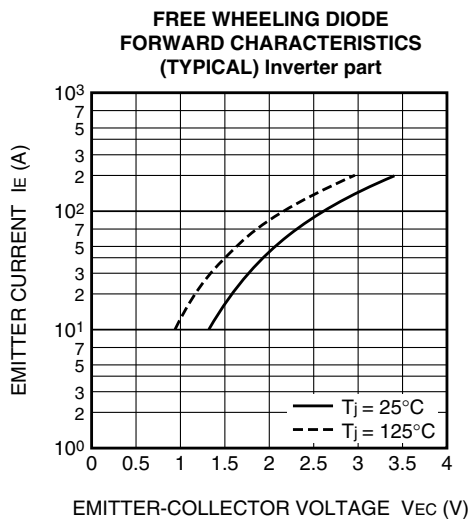
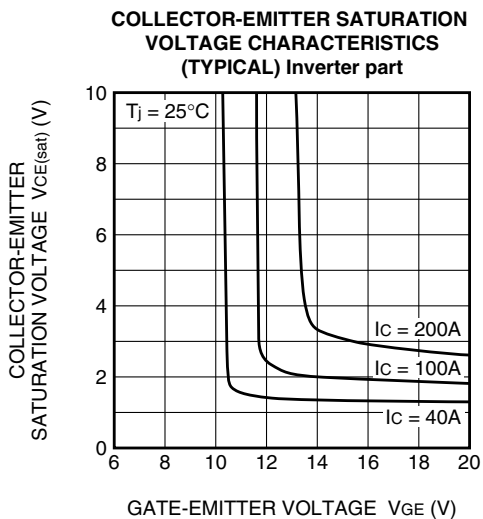
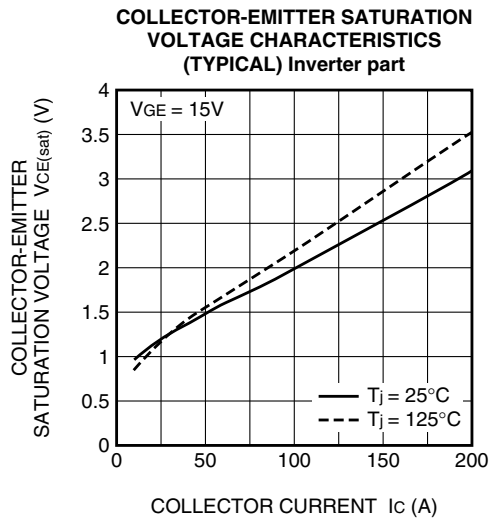
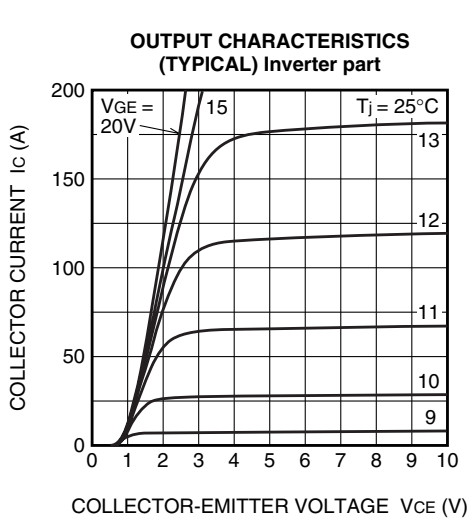


t_{rr} , Q_{rr} test waveform

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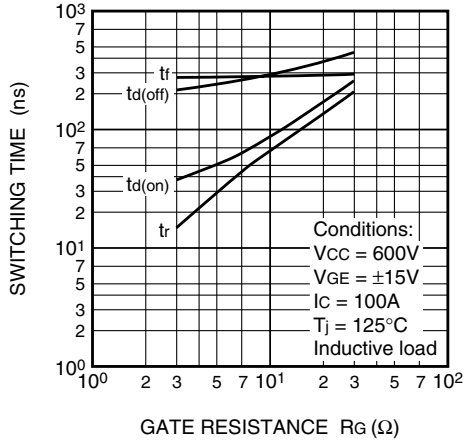
PERFORMANCE CURVES



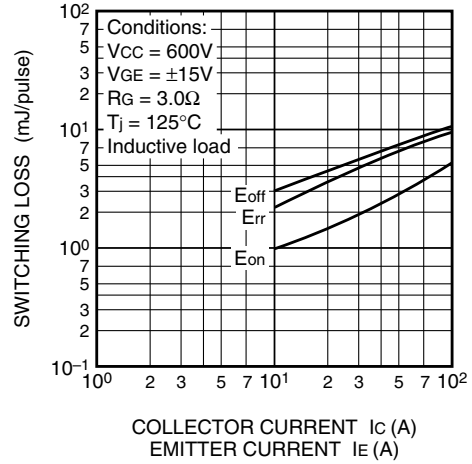
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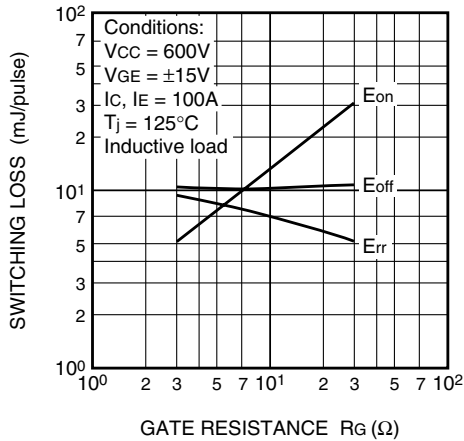
**HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL) Inverter part**



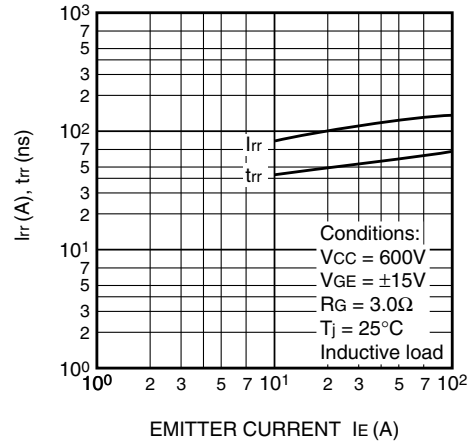
**HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL) Inverter part**



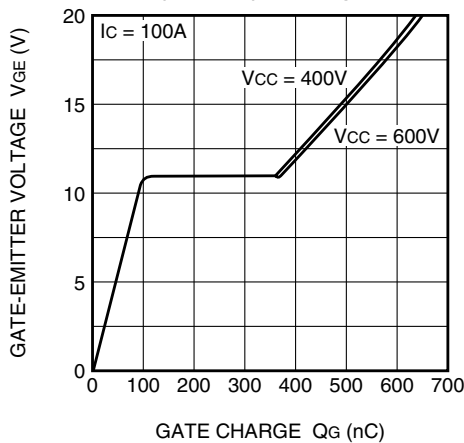
**HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL) Inverter part**



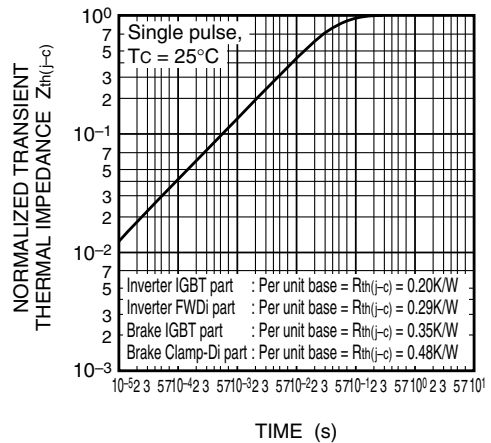
**REVERSE RECOVERY CHARACTERISTICS
OF FREE WHEELING DIODE
(TYPICAL) Inverter part**



**GATE CHARGE CHARACTERISTICS
(TYPICAL) Inverter part**



**TRANSIENT THERMAL
IMPEDANCE CHARACTERISTICS**



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HIGH POWER SWITCHING USE

